



TAN-064

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Experience *Our* Connectivity **XRT86VL3x Interface Description**

XRT86VL3X

Applications Note

Interface Description

Introduction

This application note is intended to describe the minimum pin connections for typical DS-1/E1 interfaces to EXAR's XRT86VL3x Framer + LIU combo family of products. The XRT86VL3x has many features with a wide range of flexibility. For simplification, Power, Ground, Clock/Control, and the Microprocessor are not included in this application note. Rather, it will concentrate on one channel and its connection options.

Interface Description

Figure 1 is a simplified block diagram for one channel. This diagram splits the physical interface into two distinct blocks, Required Pins and Optional Pins. For a detailed description of the pins, please refer to the most recent datasheet on EXAR's website www.exar.com.

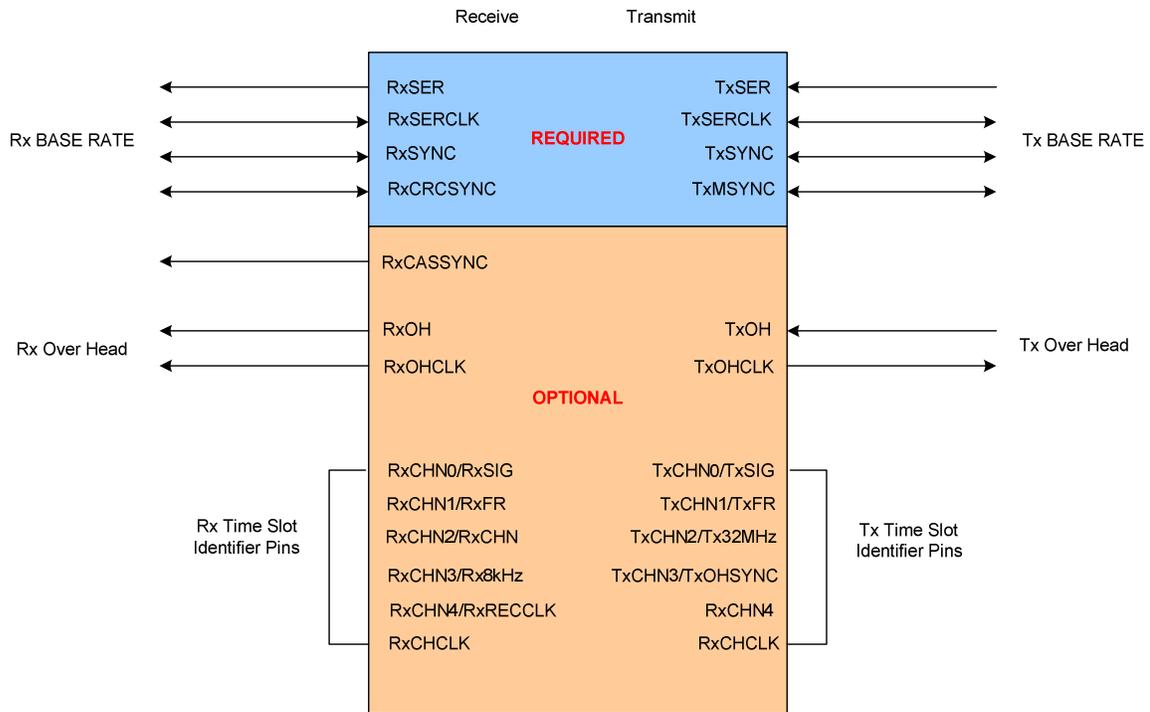


Figure 1. Simplified Block Diagram of One Channel for XRT86VL3x Framer + LIU Combo

Note1: Any input pins NOT used can be left floating or tied to Ground.

Note2: Any output or bi-directional pins NOT used can be left floating.